

**DEPARTMENT OF ELECTRICAL ENGINEERING
INDIAN INSTITUTE OF SCIENCE**

COURSES OFFERED BY EE FACULTY DURING AUG-DECEMBER 2018

It should be noted that SCC has defined MWF 1 hour slots starting at 8.00 AM/2.00 PM and TTH 1.5 hours slots starting at 8.30 AM/2.00PM for conducting course instruction classes.

Course No.	Credits	Course Title	Instructor/s	Time Slot	LAB
E1 241	3:0	DYNAMICS OF LINEAR SYSTEMS	Pavan Kumar	MWF 12-1.00	
E4 221	2:1	DSP AND AI TECHNIQUES IN POWER SYSTEM PROTECTION	U.J. Shenoy	*	
E4 231	3:0	POWER SYSTEM DYNAMICS & CONTROL	Gurunath G	T-TH 11.30-1.00	
E4 234	3:0	ADVANCED POWER SYSTEMS ANALYSIS	Sarasij Das	WF 11.30-1.00	
E5 201	2:1	PRODUCTION, MEASUREMENT AND APPLICATION OF HIGH VOLTAGE	BS Rajanikanth/ B Subba Reddy	MW 10-11	T-Th 2-5
E5 215	2:1	PULSED POWER ENGINEERING	Joy Thomas M	MW 11-12	M W 2-5
E5 253	2:1	DIELECTRICS AND ELECTRICAL INSULATION ENGG.	Joy Thomas M	MW 11-12	M W 2-5
E6 201	3:1	POWER ELECTRONICS	G. Narayanan	MW 8.30-10	M W F 2-5
E6 224	3:0	TOPICS IN POWER ELECTRONICS AND DISTRIBUTED GENERATION	Vinod John	TTH 8.30-10	
E6 225	3:0	ADVANCED POWER ELECTRONICS	Kaushik Basu	TTH 3.30-5.00	
E8 201	3:0	ELECTROMAGNETISM	Udayakumar	T-TH 10-11.30	

Course No.	Credits	Course Title	Instructor/s	Time Slot	LAB
E0 247	3:0	SENSOR NETWORKS	G.N. Rathna	*	
E0 332	3:0	MATRIX ANALYSIS	Kunal N Chaudhury	T-TH 10-11.30	
E1 222	3:0	STOCHASTIC MODELS AND APPLICATIONS	P. S Sastry	T-TH 11.00-12.30	
E1 241	3:0	DYNAMICS OF LINEAR SYSTEMS	Pavan Kumar	MWF 12-1.00	
E1 251	3:0	LINEAR AND NONLINEAR OPTIMISATION	Muthuvel Arigovindan	W,F 2 – 3.30	
E2 212	3:0	MATRIX THEORY	A.G. Ramakrishnan	MWF 9-10	
E9 201	3:0	DIGITAL SIGNAL PROCESSING	Soma Biswas/Prasanta Kumar Ghosh	T-TH 3:30-5:00	
E9 205	3:1	MACHINE LEARNING FOR SIGNAL PROCESSING	Sriram Ganapathy	MW – 3.30-5	
E9 241	2:1	DIGITAL IMAGE PROCESSING	Chandra Sekhar S	M,W – 10-11.00	
E9 245	3:1	SELECTED TOPICS IN COMPUTER VISION	Venu Madhav Govindu	M,W – 2-4.00	
E9 291	2:1	DSP SYSTEM DESIGN	G.N. Rathna	*	

- To be fixed

All classes should be held in the EE building.

Batch of students to be formed to accommodate suitably in Power Electronics and High voltage laboratory as per convenience.

(CHAIRMAN)

