

# Mid-year Research Admission in the Department of Electrical Engineering

Kindly note that there are only three seats, and that too, only for the Ph D or direct Ph D program. So, the eligibility is (i) BE/ B Tech degree with GATE score in EE/ECE/CS above a cutoff limit (a minimum of 550 but could move higher; OR, (ii) BE/ B Tech Engineering/ Technology from any centrally funded technical institutions, with a minimum overall CGPA of 8.5 out of 10 or its equivalent; OR, (iii) ME/MTech/MS/MSc (Engg) with academic performance above the cutoff limit (a minimum of 7/10 CGPA both in BE/ BTech and ME/MTech OR, with a GATE score (obtained any time before) above the cutoff limit, same as that for BE graduates.

For this year, the admission is limited to regular candidates only, and hence ERP candidates are not eligible for the mid-year admission.

The candidates will be selected purely based on their performance in the technical interview, which will cover basic subjects at the BE level relevant to the preferred areas of research and also mathematics such as matrix theory, probability and random processes. Candidates with ME degree will be asked advanced subjects relevant to the areas of research preferred.

## Choice of research areas available for mid-year admission:

The mid-year admissions with very limited seats are restricted to the following sub-areas only.

Deep learning applications to ASR and Document Analysis Systems	
Speech Processing	
Medical Signal Processing, Auditory Neuroscience	
Cyber physical systems/Internet of things	
Control theory, Networked transportation systems, smart transportation	
Computer Vision	