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Workshop on
Xilinx 7 Series FPGA's and Vivado Tool flow
22nd January, 2014

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Name: Dr/Mr/Ms _____

_____

Designation: _____

Organization: _____

Address: _____

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_____

Pincode: _____ **Phone:** _____

Mobile: _____

E-Mail: _____

Signature of the Candidate

Important Dates

- **Last date for registration:**20th Jan 2014
- **Program Date:**22nd Jan 2014

Venue:

Department of Electrical Engineering, Indian Institute of Science, Bangalore, Karnataka -560012